

List of Claims:

1. (Currently Amended) A communications riser comprising:

at least two communications devices, each of said communications devices having a differential encoder;

an integrated packet bus having three or more input data signals, one or more said input data signals being designated for carrying differentially encoded data; and

a controller for communicating data with each of said communications devices via ~~said~~ a communications bus, said controller having a differential decoder;

wherein said differential encoder encodes a portion of said data for transmission over said one or more said input data signals being designated for carrying differentially encoded data, and wherein said differential decoder decodes said portion of said data, and

wherein said integrated packet bus supports a communications protocol including a control slot and at least one data slot, each said slot having at least two bits, and wherein each of said at least two bits of said control slot selects which of said at least two bits of said at least one data slot belong to which of said at least two communications devices.

2. (Cancelled)

3. (Currently Amended) The communications system of claim 2 1, wherein said integrated packet bus includes two output data signals and four input data signals, and wherein each of said at least two bits of said control slot selects which of said at least two bits of said at least one data slot, on each of said data signals, belong to which of said at least two communications devices.

4. (Original) The communications system of claim 3, wherein said communications protocol includes two control slots, and wherein one of said control slots corresponds to said input data signals and the other of said control slots corresponds to said output data signals.

5. (Currently Amended) The communications system of claim 2 1, wherein said integrated packet bus includes three output data signals and three input data signals, and wherein each of said at least two bits of said control slot selects which of said at least two bits of said at least one data slot, on each said data signal, belong to which of said at least two communications devices.

6. (Original) The communications system of claim 1, wherein said at least two communications devices include a DSL device, a HomePNA device and a LAN device.

7. (Original) The communications system of claim 1, wherein said at least two communications devices include two broadband devices.

8. (Original) The communications system of claim 1, wherein said controller has a pull-up coupled to each of said one or more said input data signals being designated for carrying differentially encoded data.

9. (Original) The communications system of claim 1, wherein said controller performs a handshake with said integrated packet bus to identify said one or more said input data signals designated for carrying differentially encoded data.

10. (Currently Amended) An integrated packet bus for data communications between a controller and a at least two communications devices, said integrated packet bus comprising:

a receive data clock signal;

a transmit data clock signal;

a receive frame signal;
a transmit frame signal;
at least two output data signals; and
at least three input data signals;
wherein at least two or more of said at least three input data signals are not designated for carrying differentially encoded data and at least one or more of said at least three input data signals are designated for carrying differentially encoded data.

wherein one or more of said at least two output data signals carries a control slot having at least two bits and a data slot having at least two bits from said controller to said at least two said communications devices, and wherein each of said at least two bits of said control slot selects which of said at least two bits of said at least one data slot belong to which of said at least two communications devices.

11. (Original) The integrated packet bus of claim 10, wherein said at least one or more input data signals designated for carrying differentially encoded data are clocked at a higher rate than said at least two or more input data signals not designated for carrying differentially encoded data.

12. (Original) The integrated packet bus of claim 10, wherein said integrated packet bus comprises two output data signals and four input data signals.

13. (Original) The integrated packet bus of claim 10, wherein said integrated packet bus comprises three output data signals and three input data signals.

14. (Original) The integrated packet bus of claim 10, wherein said integrated packet bus comprises three output data signals and five input data signals.

15. (Cancelled)

16. (Currently Amended) A communications method for data communications on an integrated packet bus linking a controller and a at least two communications devices, said integrated packet bus having one or more input data signals designated for carrying differentially encoded data and two or more input data signals not designated for carrying differentially encoded data, said communications method comprising the steps of:

encoding a first portion of said data using a differential encoding technique;

transmitting said first portion of said data on said one or more input data signals designated for carrying differentially encoded data; **and**

transmitting a second portion of said data on said two or more input data signals not designated for carrying differentially encoded data;

transmitting a first control slot having at least two bits; and

transmitting at least one first data slot having at least two bits;

wherein said at least two bits of said first control slot indicate which of said at least two bits of said at least one first data slot belong to which of said at least two communications devices.

17. (Cancelled)

18. (Currently Amended) The communications method of claim 17 16, wherein said integrated packet bus includes two output data signals, and wherein each of said at least two bits of said first control slot selects which of said at least two bits of said at least one first data slot, on each of said output data signals, belong to which of said at least two communications devices.

19. (Currently Amended) The communications method of claim 17 16 further comprising the steps of:

receiving a second control slot having at least two bits; and
receiving at least one second data slot having at least two bits;
wherein said at least two bits of said second control slot indicate which of said at least two bits of said at least one second data slot belong to which of said at least two communications devices.

20. (Currently Amended) The communications method of claim 17 16, wherein said at least two communications devices include a DSL device, a HomePNA device and a LAN device.

21. (Currently Amended) The communications method of claim 17 16, wherein said at least two communications devices include two broadband devices.

22. (Original) The communications method of claim 16 further comprising the steps of:
receiving said first portion of said data on said one or more input data signals designated for carrying differentially encoded data;
receiving said second portion of said data on said two or more input data signals not designated for carrying differentially encoded data; and
decoding said first portion of said data using said differential encoding technique.